

DRAM (DDR2) Example: 8GB Installed In A 955 Chipset System

MCH Physical Address Translation Example: Sequential Channels, no address remap)

Physical Address Bit: 35 34 33 32 31 30 29 ----- 16 15 14 13 12 11 10 09 08 07 06 05 04 03 02 01 00
 DRAM Interface: xx xx xx CHA/B Rank 1:0 Row13----Row0 Bank 2:0 Col9-----Col0 xx xx xx

